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Digital Signal Processing Inc.

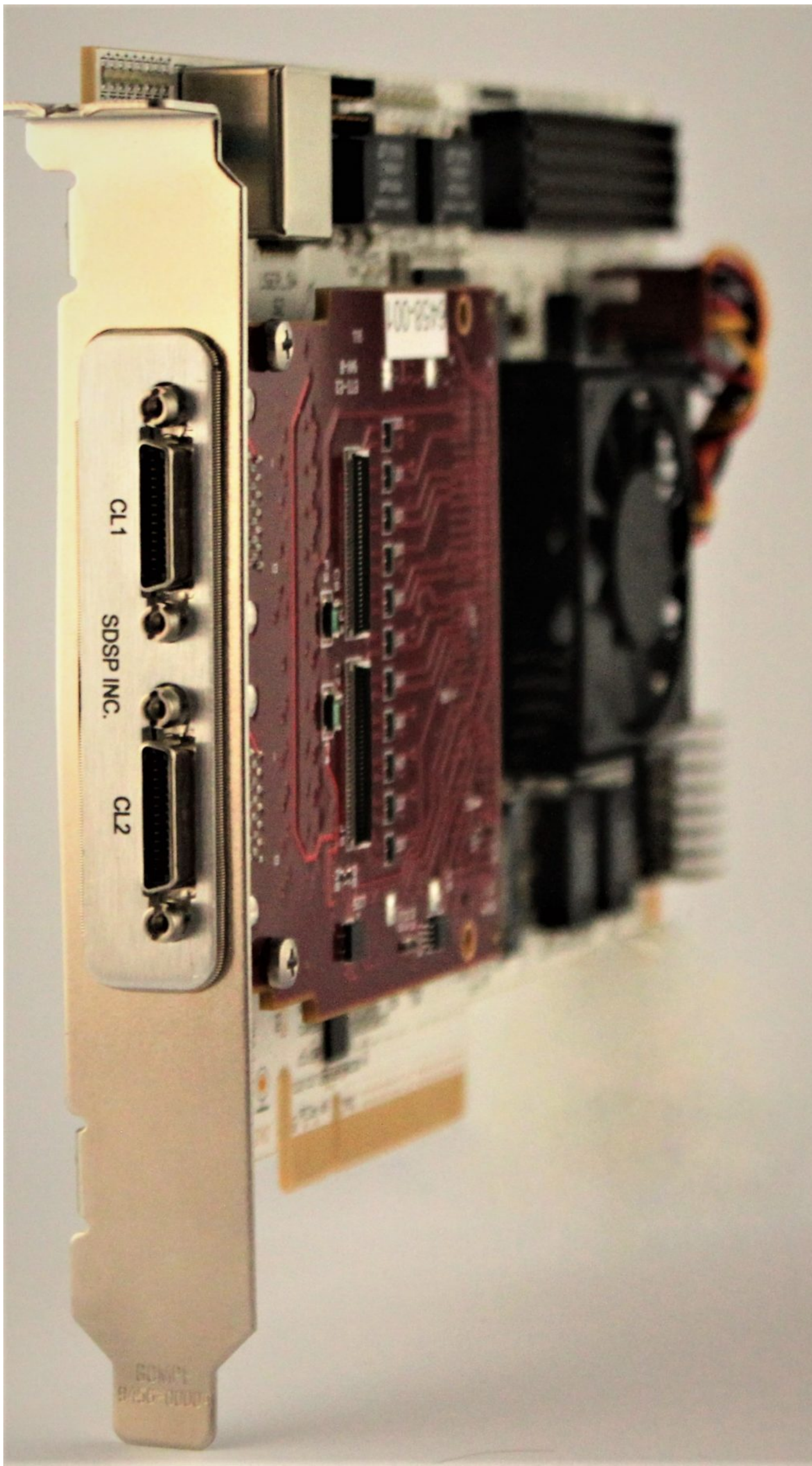


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FG-650CL, A PCIE, OPEN FPGA, BASED CAMERALINK FRAME GRABBER

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Description	Features and Applications	Additional Images	IP Cores/Drivers/BSP
Ordering Information			

FG-650CL is an FPGA based imaging solution that supports BASE, MEDIUM, FULL and Extended FULL CameraLink compatible cameras. The hardware is a PC plugin card but can also be used in an embedded fashion. The solution offers 8 lanes of Gen3 PCIe for host communication. An FMC-CL Cameralink FMC card is attached to the base card for interfacing to the cameras.

Interface	FG650CL-PCIe	Number of Cameras +(option)
Base CL	√	2 (2)
Medium CL	√	1 (1)
Full CL	√	1 (1)
Extended Full CL	√	1 (1)
2 x SATA-3	√	

The FG-650CL has a variety of I/O capabilities – x2 SATA connectors for real-time storage of image frames (SATA IP core is not included). The presence of a free FMC site gives a system developer the ability to add different functionalities to this hardware through the many available FMCs from various vendors or by adding another FMC-CL to support four Base or 2 of the Medium, Full or Extended mode cameras.

FG-650CL can be ordered with either a [Zynq US+](#) base board or with a Kintex UltraScale 060, 085, or 115 FPGA in A1517 package (Kinex US+ base board is not recommended for new designs) and includes:

- 8 Lane PCIe Gen3
- JTAG
- **Free FMC site (HPC)**
- 72 bits wide, 2.5GB DDR4 2400, and config. FLASH

With an open PL part, fully user- programmable to implement customized IP cores. The ARM processor on the MPSOC is also

programmable by user.

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